Electronic Acknowledgement Receipt				
EFS ID:	1036732			
Application Number:	09620649			
Confirmation Number:	4313			
Title of Invention:	INTEGRATED CIRCUIT HAVING INDEPENDENTLY FORMED ARRAY AND PERIPHERAL ISOLATION DIELECTRICS			
First Named Inventor:	Cetin Nmi Kaya			
Correspondence Address:	Jacqueline J Garner Esq Texas Instruments Incorporated P O Box 655474 M S 219 - Dallas TX 75265 US 9732789697			
Filer:	Wade J. Brady III/Gracia Sansom			
Filer Authorized By:	Wade J. Brady III			
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Application Type:	Utility			
International Application Number:				
Payment information:				

## Payment information:

Submitted with Payment	no
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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		T23686-1-SB96-SB80.pdf	9789	yes	2		
	Multipart Description						
	Doc Desc		Start	End			
	Assignee showing of ownership per 37 CFR 3.73(b).		1	1			
	Power of Attorney (may include Associate POA)		2	2			
Warnings:			1				
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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.